IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

(F2)

plicant:

Luiz M. Franca-Neto

Title MADEUS AMPLIFICATION APPARATUS, SYSTEMS, AND METHODS

Docket No.:

884.B65US1

Serial No.: 10/749927

Filed:

December 29, 2003

Due Date: N/A

Examiner:

Unknown

Group Art Unit: Unknown

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

We are transmitting herewith the following attached items (as indicated with an "X"):

- X A return postcard.
- X A Communication Concerning Related Applications (1 pg.).
- X An Information Disclosure Statement (2 pgs.), Form 1449 (1 pg.), and copies of 2 cited documents (Copies of US Patent documents not enclosed; application filed after June 30, 2003).

If not provided for in a separate paper filed herewith, Please consider this a PETITION FOR EXTENSION OF TIME for sufficient number of months to enter these papers and please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

Customer Number 21186

Atty: Ann M. McCrackin

Reg. No. 42,858

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this ______ day of ______, 2004.

FACIA LEE

Name

Signature Kacia Lee

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

(GENERAL)

10/749927

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Luiz M. Franca-Neto

Examiner: Unknown

Serial No.:

10/749927

Group Art Unit: Unknown

Filed:

December 29, 2003

Docket: 884.B65US1

Title:

AMPLIFICATION APPARATUS, SYSTEMS, AND METHODS

Assignee:

Intel Corporation

Customer No.: 21186

COMMUNICATION CONCERNING RELATED APPLICATION(S)

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Applicant would like to bring to the Examiner's attention the following related application(s) in the above-identified patent application:

Serial/Patent No.

Filing Date

Attorney Docket

Title

10/739468

December 18, 2003

884.B62US1

COMPONENT PACKAGING

APPARATUS, SYSTEMS, AND

METHODS

Respectfully submitted,

LUIZ M. FRANCA-NETO

By Applicant's Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

Attorneys for Intel Corporation

P.O. Box 2938

Minneapolis, MN 55402

(612) 349-9592

Date June 2, 200

Reg. No. 42,858

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 4 day of June

ACIA LEE

Kacia Lee

S/N 10/749927 PATENT

IN THE LINE STATES PATENT AND TRADEMARK OFFICE

Applicant: Luiz M. Franca-Neto Examiner: Unknown

Serial No.: 10/749927 Group Art Unit: Unknown
Filed: December 29, 2003 Docket: 884.B65US1

Filed: December 29, 2003 Docket: 884.B65UTitle: AMPLIFICATION APPARATUS, SYSTEMS, AND METHODS

Assignee: Intel Corporation Customer No: 21186

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 et. seq., the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicant respectfully requests that this Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicant requests that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicant with the next official communication.

Pursuant to 37 C.F.R. §1.97(b), it is believed that no fee or statement is required with the Information Disclosure Statement. However, if an Office Action on the merits has been mailed, the Commissioner is hereby authorized to charge the required fees to Deposit Account No. 19-0743 in order to have this Information Disclosure Statement considered.

The present application is either a U.S. national patent application filed after June 30, 2003 or an international application that entered the national stage under 35 U.S.C. § 371 after June 30, 2003. Thus, Applicant believes that the U.S. Patent & Trademark Office has waived the requirement under 37 C.F.R. 1.98 (a)(2)(i) for submitting a copy of each cited U.S. patent and each U.S. patent application publication. The waiver is provided in a pre-OG notice from the U.S. Patent & Trademark Office entitled "Information Disclosure Statements May Be Filed Without Copies of U.S. Patents and Published Applications in Patent Applications filed after June 30, 2003" and dated July 11, 2003. Applicant acknowledges the requirement to submit copies of foreign patent documents and non-patent literature in accordance with 37 C.F.R. 1.98(a)(2).

Filing Date: December 29, 2003

Title: AMPLIFICATION APPARATUS, SYSTEMS, AND METHODS

Assignee: Intel Corporation

The Examiner is invited to contact the Applicant's Representative at the below-listed telephone number if there are any questions regarding this communication.

Respectfully submitted,

LUIZ M. FRANCA-NETO

By his Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A. Attorneys for Intel Corporation P.O. Box 2938
Minneapolis, Minnesota 55402

Page 2

Dkt: 884.B65US1 (INTEL)

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Date June 2, 2004

Ann M. McCrackin

Reg. No. 42,858

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Signature Lacia Loo

PTO/SB/08A(10-01)
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or the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number, Substitute for form 1449A/PTO Complete if Known INFORMATION DISCLOSURE **Application Number** 10/749927 STATEMENT BY APPLICANT December 29, 2003 Filing Date Franca-Neto, Luiz **First Named Inventor Group Art Unit** Unknown Unknown **Examiner Name** Attorney Docket No: 884.B65US1

US PATENT DOCUMENTS									
Examiner Initial *			Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate			
	US-6,072,364	06/06/2000	Jeckeln, Ernesto G., et al.	330	149	03/17/1997			

FOREIGN PATENT DOCUMENTS									
Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T²			

OTHER DOCUMENTS NON PATENT LITERATURE DOCUMENTS					
Examiner Initials*	Cite No Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), titl (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue n publisher, city and/or country where published.		T²		
		FRANCA-NETO, L. M., "Enabling High-Performance Mixed-Signal System-on-a-Chip (SoC) in High Performance Logic CMOS Technology", 2002 Symposium on VLSI Circuits Digest of Technical Papers, 164-167			
		FRANCA-NETO, L. M., "System-on-a-package (SoP) Solution for High Performance RF/Microwave Systems", Progress in Electromagnetic Research Symposium, PIERS 2002, 494			

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